

Introduction

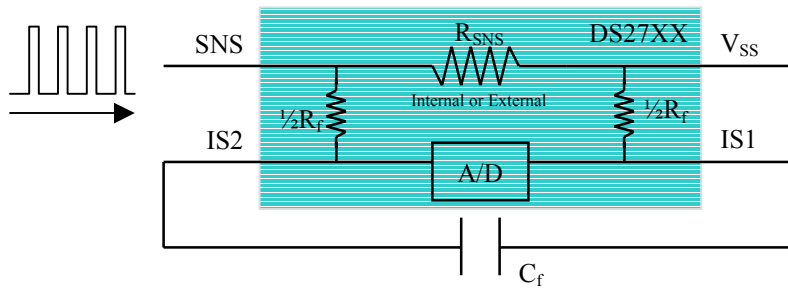


Figure 1. DS27XX A/D Filter Circuit

The data sheet specified maximum allowable input range for the current sensing A/D on DS27XX fuel gauge devices is displayed as a constant current value. This is not the true peak current measurable however. The A/D input of every Dallas Semiconductor fuel gauge contains an RC filter that will smooth the waveform and allow peaks greater than the specified maximum to be properly integrated. The filter capacitance is selectable to produce a waveform that uses the full range of the A/D but does not saturate its input.

Operation

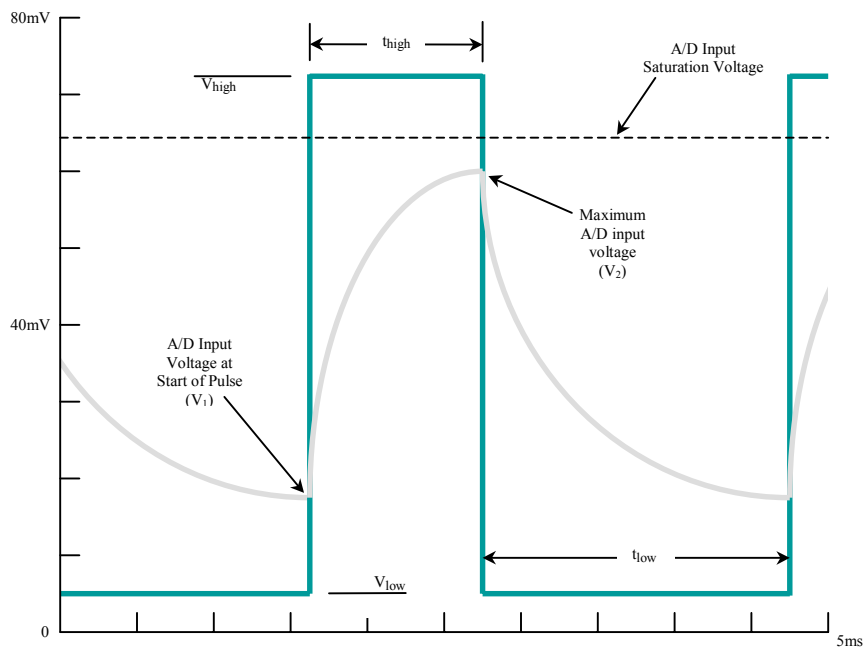


Figure 2. Filtered and Unfiltered Waveforms

The filter resistance is internal to the device and cannot be altered; the filter capacitor is connected between the IS1 and IS2 inputs. A value for the filter capacitor should be chosen such that the full range of the A/D is utilized without exceeding the input limit. The example in Figure 2 shows an original waveform greater than the A/D input range and the filtered waveform with a maximum peak (V_2) less than the A/D input range.

The ideal value for the filter capacitor can either be determined experimentally or mathematically. The maximum filtered voltage can be represented in terms of the filter components and input waveform parameters. Equation 1 shows how the maximum filtered voltage is derived assuming the pattern is repetitive and the filtered waveform has reached a steady state. From this equation a capacitor value can be selected that meets the requirements of the application. The derivation of Equation 1 is shown in the appendix.

Equation 1. Calculation of the Maximum Filtered Voltage

$$V_2 = \frac{V_{high} \cdot \left(1 - e^{-\frac{t_{high}}{R_f \cdot C_f}}\right) + V_{low} \cdot e^{-\frac{t_{high}}{R_f \cdot C_f}} \cdot \left(1 - e^{-\frac{t_{low}}{R_f \cdot C_f}}\right)}{\left(1 - e^{-\frac{t_{low} + t_{high}}{R_f \cdot C_f}}\right)}$$

Where:

- V_1 = Minimum voltage of the filtered waveform
- V_2 = Maximum voltage of the filtered waveform
- V_{low} = Minimum voltage of the input waveform
- V_{high} = Maximum voltage of the input waveform
- t_{low} = The low period of the input waveform
- t_{high} = The high period of the input waveform
- R_f = The filter resistance inside the DS27XX device
- C_f = The selected external filter capacitance

For example, if a DS2761 with a filter resistance (R_f) of 9.4k Ω (two 4.7k Ω resistors in series) and input voltage limit of 64mV is used to measure the waveform in Figure 1 ($V_{low} = 6\text{mV}$, $V_{high} = 74\text{mV}$, $t_{low} = 2.0\text{ms}$, $t_{high} = 1.125\text{ms}$), a filter capacitance (C_f) of at least .068 μF is needed to prevent A/D input saturation. This calculation does not include device or waveform tolerances. Assume a filter resistance tolerance of $\pm 15\%$ of the listed datasheet value.

Summary

DS27XX-based fuel gauges can measure current pulses much greater than their specified input range by filtering the waveform to be measured. The shape of the waveform is altered, changing individual current measurements, but the waveform energy is maintained keeping the accumulated current measurement accurate.

Appendix—C_f Calculation Equations

Given the input waveform in Figure 1, the maximum voltage seen by the Current A/D can be calculated mathematically. For the filtered waveform to be at a steady state, the voltage increase during the high period of the pulse must equal the voltage decrease during the low period of the pulse. The filtered peak voltage can therefore be related to the filtered minimum voltage by the following equations:

$$V_1 = V_{low} + (V_2 - V_{low}) \cdot e^{\frac{-t_{low}}{R_f \cdot C_f}}$$

$$V_2 = V_{high} + (V_1 - V_{high}) \cdot e^{\frac{-t_{high}}{R_f \cdot C_f}}$$

Where:

- V_1 = Minimum voltage of the filtered waveform
- V_2 = Maximum voltage of the filtered waveform
- V_{low} = Minimum voltage of the input waveform
- V_{high} = Maximum voltage of the input waveform
- t_{low} = The low period of the input waveform
- t_{high} = The high period of the input waveform
- R_f = The filter resistance inside the DS27XX device
- C_f = The selected external filter capacitance

Substituting for V_1 by combining the two equations produces:

$$V_2 = V_{high} + \left[V_{low} + (V_2 - V_{low}) \cdot e^{\frac{-t_{low}}{R_f \cdot C_f}} - V_{high} \right] \cdot e^{\frac{-t_{high}}{R_f \cdot C_f}}$$

Which can then be reduced to:

$$V_2 = \frac{V_{high} \cdot \left(1 - e^{\frac{-t_{high}}{R_f \cdot C_f}} \right) + V_{low} \cdot e^{\frac{-t_{high}}{R_f \cdot C_f}} \cdot \left(1 - e^{\frac{-t_{low}}{R_f \cdot C_f}} \right)}{\left(1 - e^{\frac{-t_{low} + -t_{high}}{R_f \cdot C_f}} \right)}$$

Solving for C_f beyond this point becomes mathematically complex. An iterative process can be used to determine a value for C_f that limits the maximum voltage V_2 to below the saturation point of the A/D. Remember to include device tolerances when calculating a value for C_f .